



Attorney Docket No.: 60188-084  
PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

#10/Ex  
3/20/3  
Lusker

In re Application of

Shigeki FURUYA, et al.

Serial No.: 09/922,671

Filed: August 07, 2001

Attn: BOX AF

Group Art Unit: 2815

Examiner: Matthew E. WARREN

For: CMOS BASIC CELL AND METHOD FOR FABRICATING SEMICONDUCTOR  
INTEGRATED CIRCUIT USING THE SAME

PETITION FOR EXTENSION OF TIME  
PURSUANT TO 37 C.F.R. §§ 1.136(a) AND 1.17(a)(2)

Hon. Assistant Commissioner for Patents  
Washington, D.C. 20231

RECEIVED  
MAR 18 2003  
TECHNOLOGY CENTER 2800

Sir:

Please extend the time for response to the final Office Action dated  
October 15, 2002 set to expire January 15, 2003 for an additional two months, i.e., up  
to and including March 15, 2003.

Please charge the amount of \$410.00 as required by 37 C.F.R. §1.17(a)(2) to  
Deposit Account No. 50-0417 to cover the Petition fee. A Request for Continued  
Examination Under 37 C.F.R. 1.114 is being filed concurrently herewith.

The Commissioner is hereby authorized to charge any additional fees associated

Serial No.: 09/922,671


with this communication or credit any overpayment to Deposit Account No. 50-0417. A duplicate copy of this Petition is enclosed for accounting purposes.

Respectfully submitted,

McDERMOTT, WILL & EMERY

Date: 3/14/03

By:

  
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